

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	63417	pcb	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:22
L2	149119	"printed circuit board"	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:23
L3	58674	equalizer or equalization	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:23
L4	84	2 same 3	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L5	2459	pre-emphasis	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L6	614749	transmit	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L7	58674	equalizer or equalization	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L8	1440222	transmission	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L9	104	L8 adj L7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L10	38	L6 adj L7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:27
L11	142	L9 or L10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:28
L12	4	5 and 11	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:28

S1	614749	transmit	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 10:57
S2	58674	equalizer or equalization	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 10:57
S3	38	S1 adj S2	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 10:57
S4	0	395/296.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 10:57
S5	657	375/296.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 10:57
S6	160	S2 and S5	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 11:03
S7	2557	375/229-236.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:41
S8	3	S3 and S7	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 11:03
S9	5673	wander	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:41
S10	2546	predistort\$	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:41
S11	0	S9 with S10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:41
S12	3	S9 and S10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:41

S13	614749	transmit	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:57
S14	58674	equalizer or equalization	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:43
S15	735	S13 with S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:43
S16	51	S9 with S13	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:43
S17	6	S15 and S16	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:44
S18	1744386	step	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:45
S19	818	S14 near2 S18	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:46
S20	0	S9 same S10	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:47
S21	55	S9 with S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:47
S22	657	375/296.ccls.	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:51
S23	2346527	amplitude or level	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:51
S24	174359	adjust?	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:51

S25	6616	S23 near2 S24	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:52
S26	25	S22 and S25	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:52
S27	1440222	transmission	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:57
S28	104	S27 adj S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:58
S29	38	S13 adj S14	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 13:58
S30	142	S28 or S29	USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/04 15:22

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
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 Itoh, N.; Tokumoto, T.;
 Consumer Electronics, IEEE Transactions on
 Volume 36, Issue 3, Aug 1990 Page(s):238 - 246
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- ☐ 2. **Mixed-signal equalization architectures for printed circuit board channels**
 Hoyos, S.; Garcia, J.A.; Arce, G.R.;
 Circuits and Systems I: Regular Papers, IEEE Transactions on [see also Circuits and Systems I: Fundamental Theory and Applications, IEEE Transactions on]
 Volume 51, Issue 2, Feb. 2004 Page(s):264 - 274
[AbstractPlus](#) | Full Text: [PDF](#)(528 KB) IEEE JNL
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 Weiguang Hou; Kwon, H.M.;
 Instrumentation and Measurement, IEEE Transactions on
 Volume 48, Issue 1, Feb. 1999 Page(s):34 - 38
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(180 KB) IEEE JNL
- ☐ 4. **Multiprocessor Implementation of Adaptive Digital Filters**
 Lawrence, V.; Tewksbury, S.;
 Communications, IEEE Transactions on [legacy, pre - 1988]
 Volume 31, Issue 6, Jun 1983 Page(s):826 - 835
[AbstractPlus](#) | Full Text: [PDF](#)(920 KB) IEEE JNL
- ☐ 5. **Pao networks for data transmission equalization**
 Arcens, S.; Cid-Sueiro, J.; Figueiras-Vidal, A.R.;
 Neural Networks, 1992. IJCNN., International Joint Conference on
 Volume 2, 7-11 June 1992 Page(s):963 - 968 vol.2
[AbstractPlus](#) | Full Text: [PDF](#)(312 KB) IEEE CNF
- ☐ 6. **Block transmission equalizers using constrained minimum variance filters with application to MC-CDM**
 Bury, A.; Lindner, J.;
 Spread Spectrum Techniques and Applications, 2000 IEEE Sixth International Symposium on
 Volume 1, 6-8 Sept. 2000 Page(s):159 - 163 vol.1
[AbstractPlus](#) | Full Text: [PDF](#)(392 KB) IEEE CNF
- ☐ 7. **A serial-link transceiver based on 8 GS/s A/D and D/A converters in 0.25 μ m CMOS**
 Ellersick, W.; Yang, C.-K.K.; Stojanovic, V.; Modjtahedi, S.; Horowitz, M.A.;
 Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 2001 IEEE International
 5-7 Feb. 2001 Page(s):58 - 59, 430
[AbstractPlus](#) | Full Text: [PDF](#)(208 KB) IEEE CNF
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9. MISO antenna array OC pre-RAKE transmitter with MRC post-RAKE receiver

Burke, J.P.; Rao, B.D.; Zeidler, J.R.;

Signals, Systems and Computers, 2002. Conference Record of the Thirty-Sixth Asilomar Conference on
Volume 2, 3-6 Nov. 2002 Page(s):1699 - 1703 vol.2


[AbstractPlus](#) | Full Text: [PDF](#)(372 KB)  CNF



10. Mixed-signal equalization architectures for printed circuit board channels

Hoyos, S.; Garcia, J.; Arce, G.;

Acoustics, Speech, and Signal Processing, 2002. Proceedings. (ICASSP '02). IEEE International Conference on
Volume 4, 13-17 May 2002 Page(s):IV-3768 - IV-3771 vol.4


[AbstractPlus](#) | Full Text: [PDF](#)(401 KB)  CNF



11. Time-domain method for tracking dispersive channels in OFDM systems

Roman, T.; Enescu, M.; Koivunen, V.;

Vehicular Technology Conference, 2003. VTC 2003-Spring. The 57th IEEE Semiannual
Volume 2, 22-25 April 2003 Page(s):1318 - 1321 vol.2


[AbstractPlus](#) | Full Text: [PDF](#)(334 KB)  CNF



12. Adaptive equalization and data recovery in a dual-mode (PAM2/4) serial link transceiver

Stojanovic, V.; Ho, A.; Garlepp, B.; Chen, F.; Wei, J.; Alon, E.; Werner, C.; Zerbe, J.; Horowitz, M.A.;

VLSI Circuits, 2004. Digest of Technical Papers. 2004 Symposium on
17-19 June 2004 Page(s):348 - 351

[AbstractPlus](#) | Full Text: [PDF](#)(445 KB)  CNF

